



**RESPONSE UNDER 37 C.F.R. 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2815**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Ference et al.

Confirmation No.: 9501

Serial No.: 09/732,240

Group Art Unit: 2815

Filed: 12/07/00

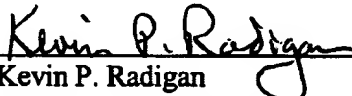
Examiner: Chris C. Chu

Title: MULTI-CHIP STACK AND METHOD OF FABRICATION UTILIZING  
SELF-ALIGNING ELECTRICAL CONTACT ARRAY

O.k.  
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C.C.  
10/29/04

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with  
the U.S. Postal Service as first class mail in an envelope addressed  
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Kevin P. Radigan  
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Date of Signature: October 19, 2004.

To: Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Response to Final Office Action**

Dear Sir:

This paper is submitted in reply to the final Office Action mailed September 21, 2004, in  
connection with the above-designated application. Allowance of all claims is respectfully  
requested in view of the amendments to claim 30 and remarks below.

**Amendments to the claims begin on page 2 of this paper.**

**Remarks being on page 5 of this paper.**